

PATENT NUMBER

U.S. UTILITY Patent Application

<div>12W O.I.P.E. SCANNED <i>pmw</i> 2 <i>pmw</i> Q.A.</div>	PATENT DATE
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APPLICATION NO. 09/540591	CONT/PRIOR	CLASS 275 370	SUBCLASS 503	ART UNIT 2704 2665	EXAMINER JUNG KIM
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APPLICANTS

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TITLE

Bus control module for a multi-stage clock distribution scheme in a signaling server

PTO-2040
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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